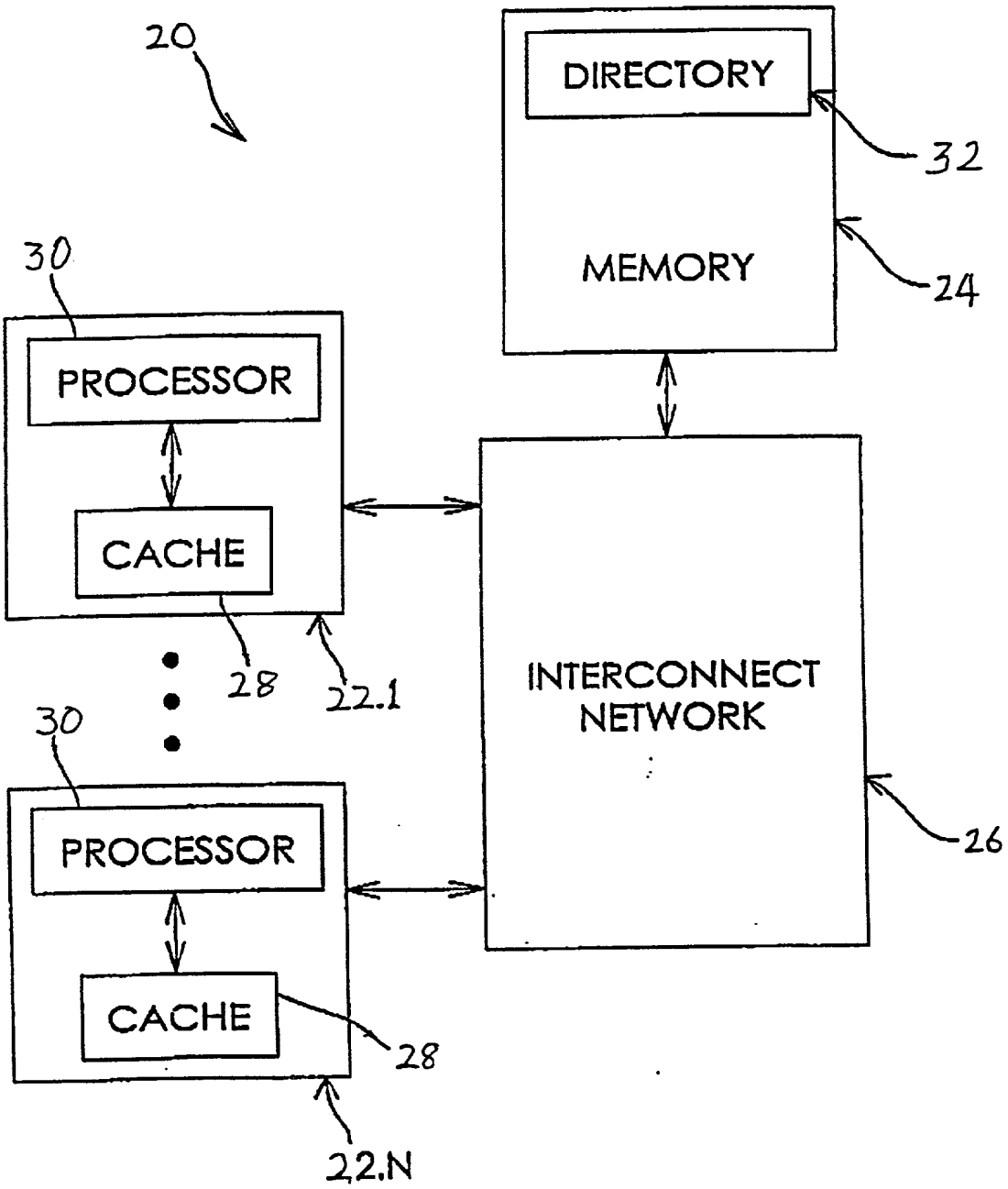

20
DIRECTORY
32
MEMORY
24
30
PROCESSOR
CACHE
28
22.1
INTERCONNECT
NETWORK
26
30
PROCESSOR
CACHE
28
22.N

Fig. 1

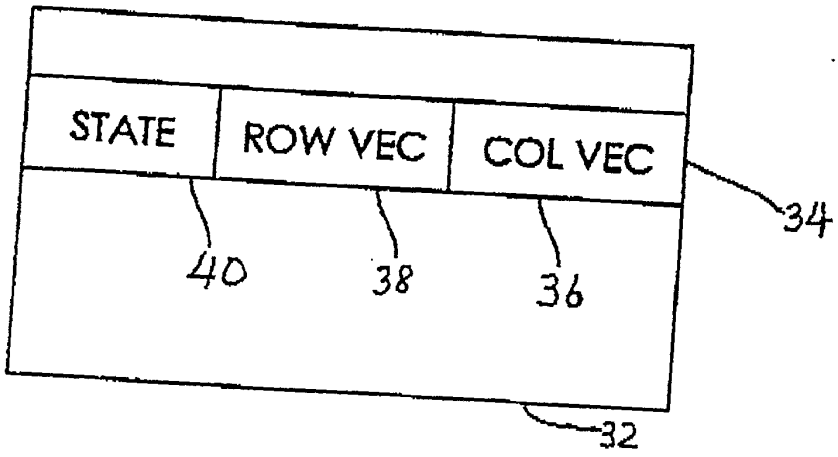

STATE
ROW VEC
COL VEC
34
40
38
36
32

Fig. 2

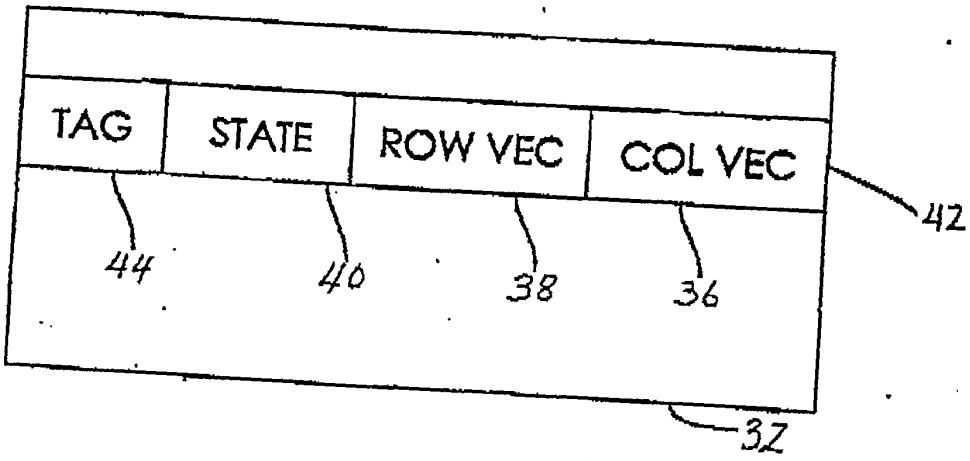

TAG
STATE
ROW VEC
COL VEC
42
44
40
38
36
32

Fig. 3

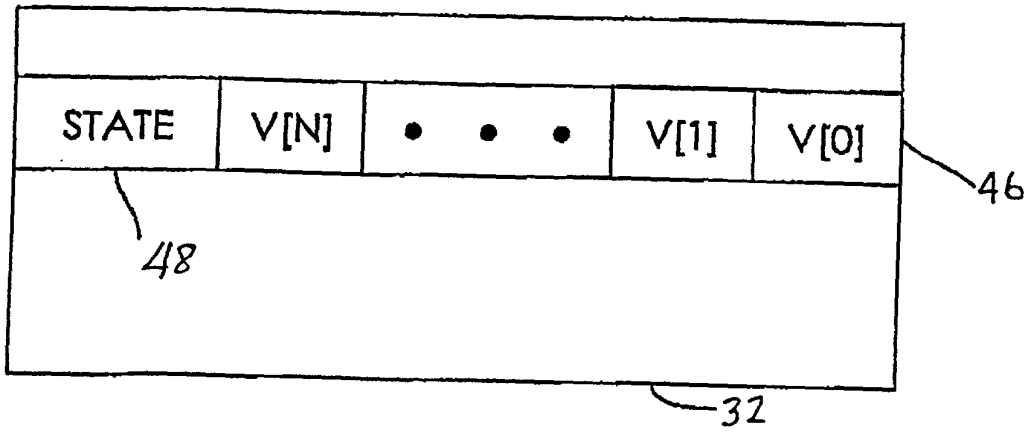


Fig. 4

Express Mail No. EL500928099US

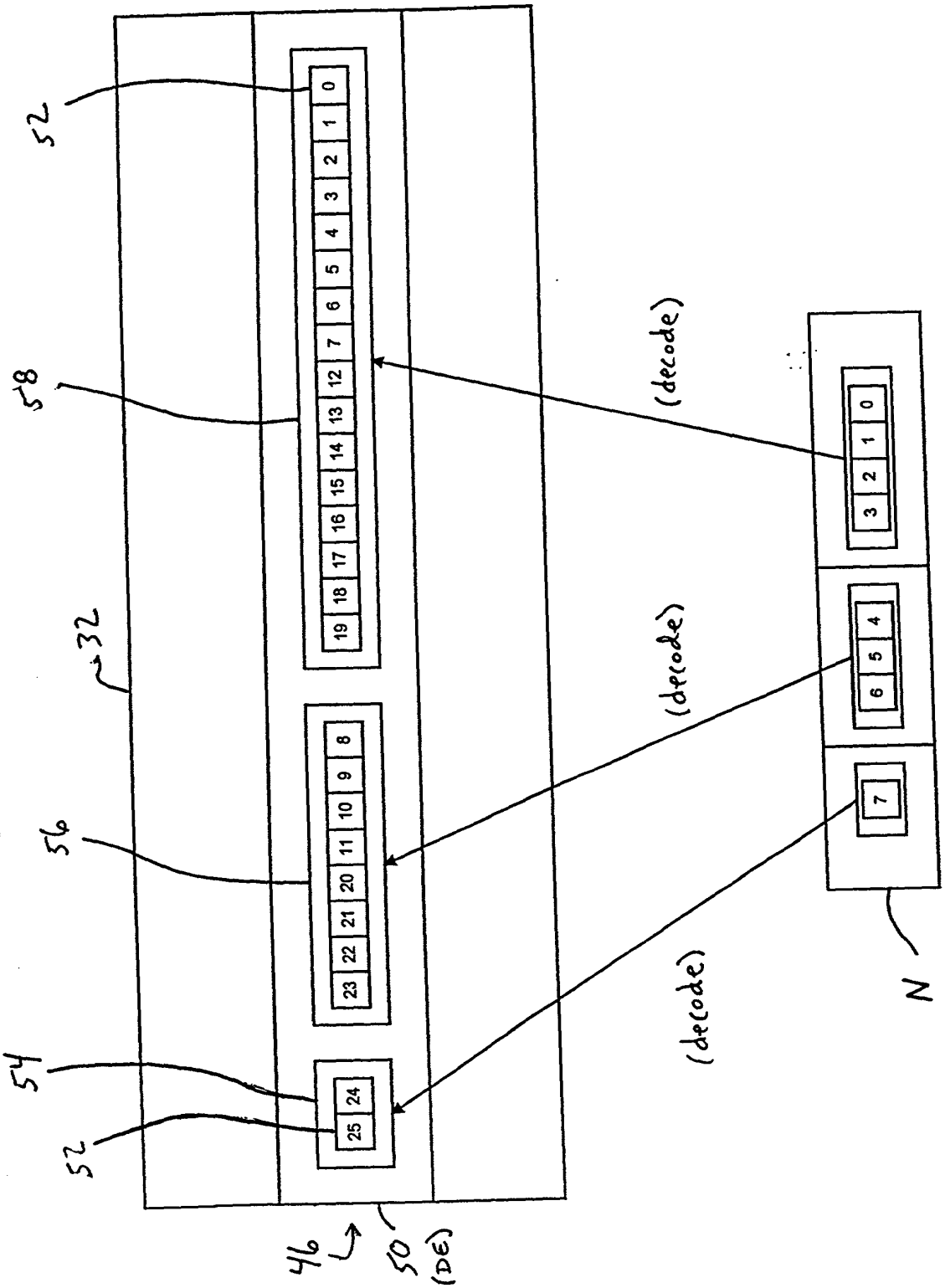

52
58
32
56
54
52
46
50
(DE)
(decode)
(decode)
(decode)
N
0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19
20 21 22 23
24 25
0 1 2 3
4 5 6
7

FIG. 5

	TOP	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
68	0	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F
68	1	10	11	12	13	14	15	16	17	18	19	1A	1B	1C	1D	1E	1F
68	2	20	21	22	23	24	25	26	27	28	29	2A	2B	2C	2D	2E	2F
68	3	30	31	32	33	34	35	36	37	38	39	3A	3B	3C	3D	3E	3F
68	4	40	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	4E	4F
68	5	50	51	52	53	54	55	56	57	58	59	5A	5B	5C	5D	5E	5F
68	6	60	61	62	63	64	65	66	67	68	69	6A	6B	6C	6D	6E	6F
68	7	70	71	72	73	74	75	76	77	78	79	7A	7B	7C	7D	7E	7F
	BOT	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
68	0	80	81	82	83	84	85	86	87	88	89	8A	8B	8C	8D	8E	8F
68	1	90	91	92	93	94	95	96	97	98	99	9A	9B	9C	9D	9E	9F
68	2	A0	A1	A2	A3	A4	A5	A6	A7	A8	A9	AA	AB	AC	AD	AE	AF
68	3	B0	B1	B2	B3	B4	B5	B6	B7	B8	B9	BA	BB	BC	BD	BE	BF
68	4	C0	C1	C2	C3	C4	C5	C6	C7	C8	C9	CA	CB	CC	CD	CE	CF
68	5	D0	D1	D2	D3	D4	D5	D6	D7	D8	D9	DA	DB	DC	DD	DE	DF
68	6	E0	E1	E2	E3	E4	E5	E6	E7	E8	E9	EA	EB	EC	ED	EE	EF
68	7	F0	F1	F2	F3	F4	F5	F6	F7	F8	F9	FA	FB	FC	FD	FE	FF

70 70 70 70 70 70 70 70 70 70 70 70 70 70 70 70 70 60

62 76 62 62 76

14 04 66

F16. 6

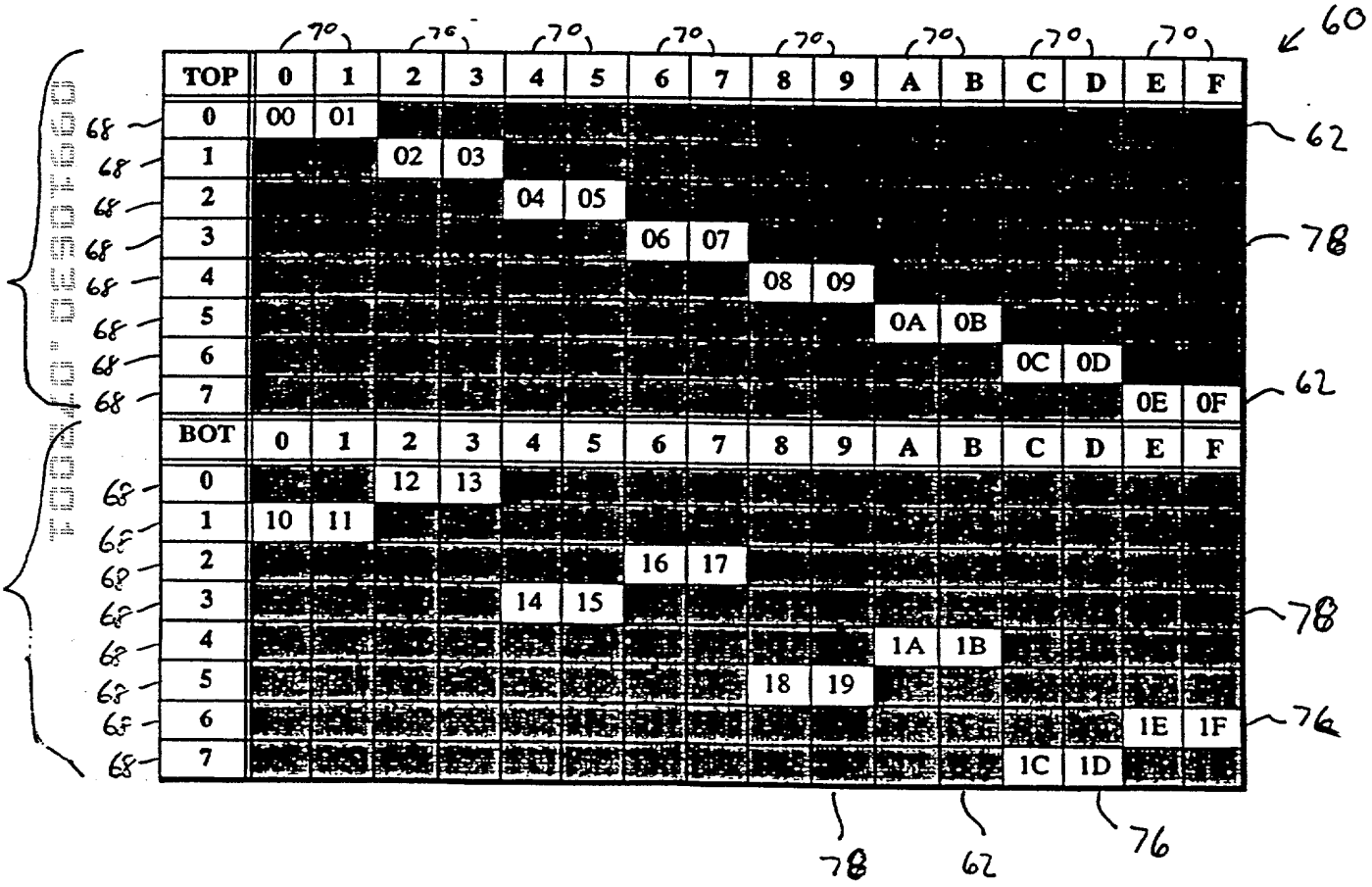

60
70
68
62
78
76
TOP
BOT
00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 0F
10 11 12 13 14 15 16 17 18 19 1A 1B 1C 1D 1E 1F

FIG. 7

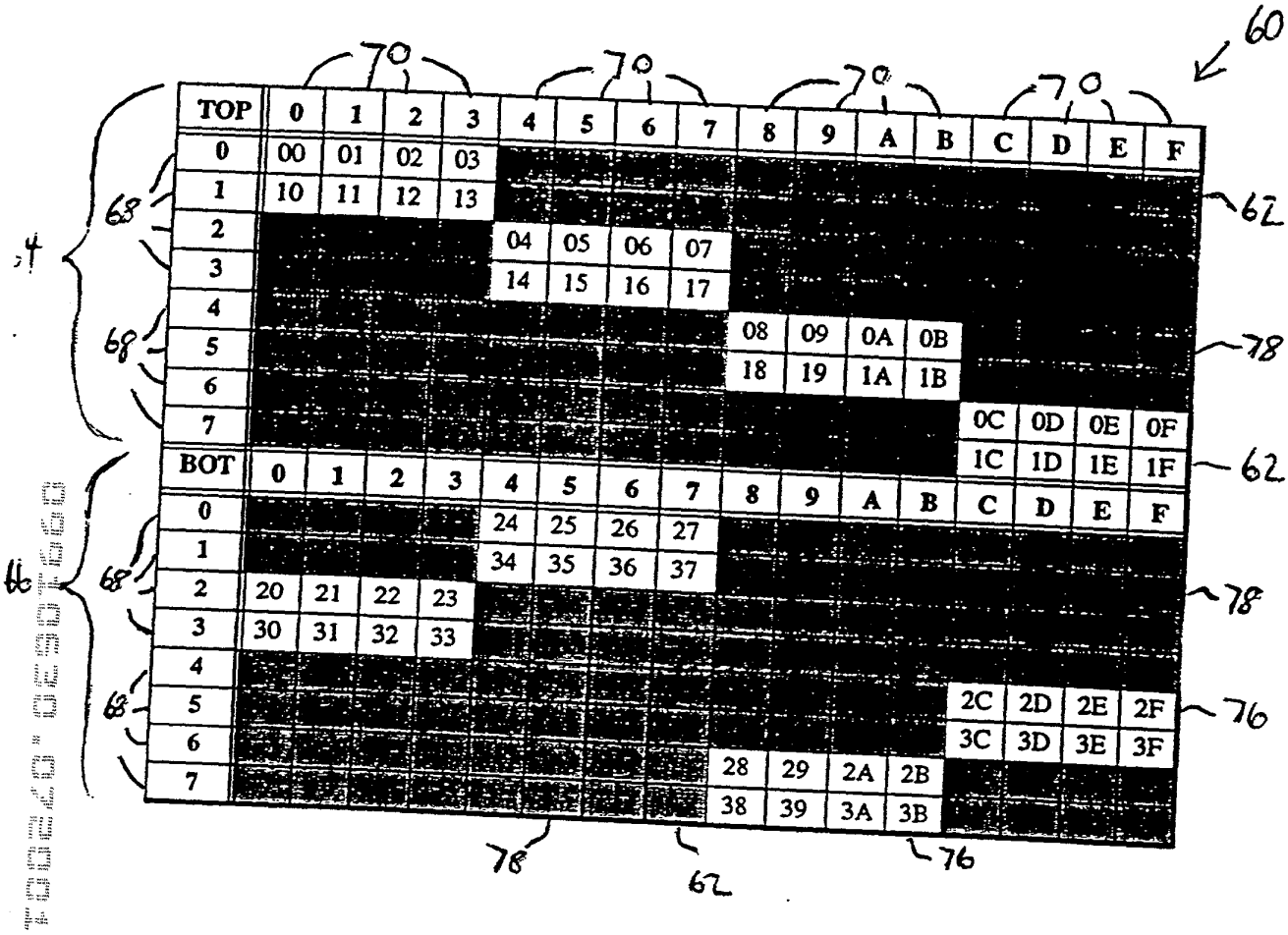
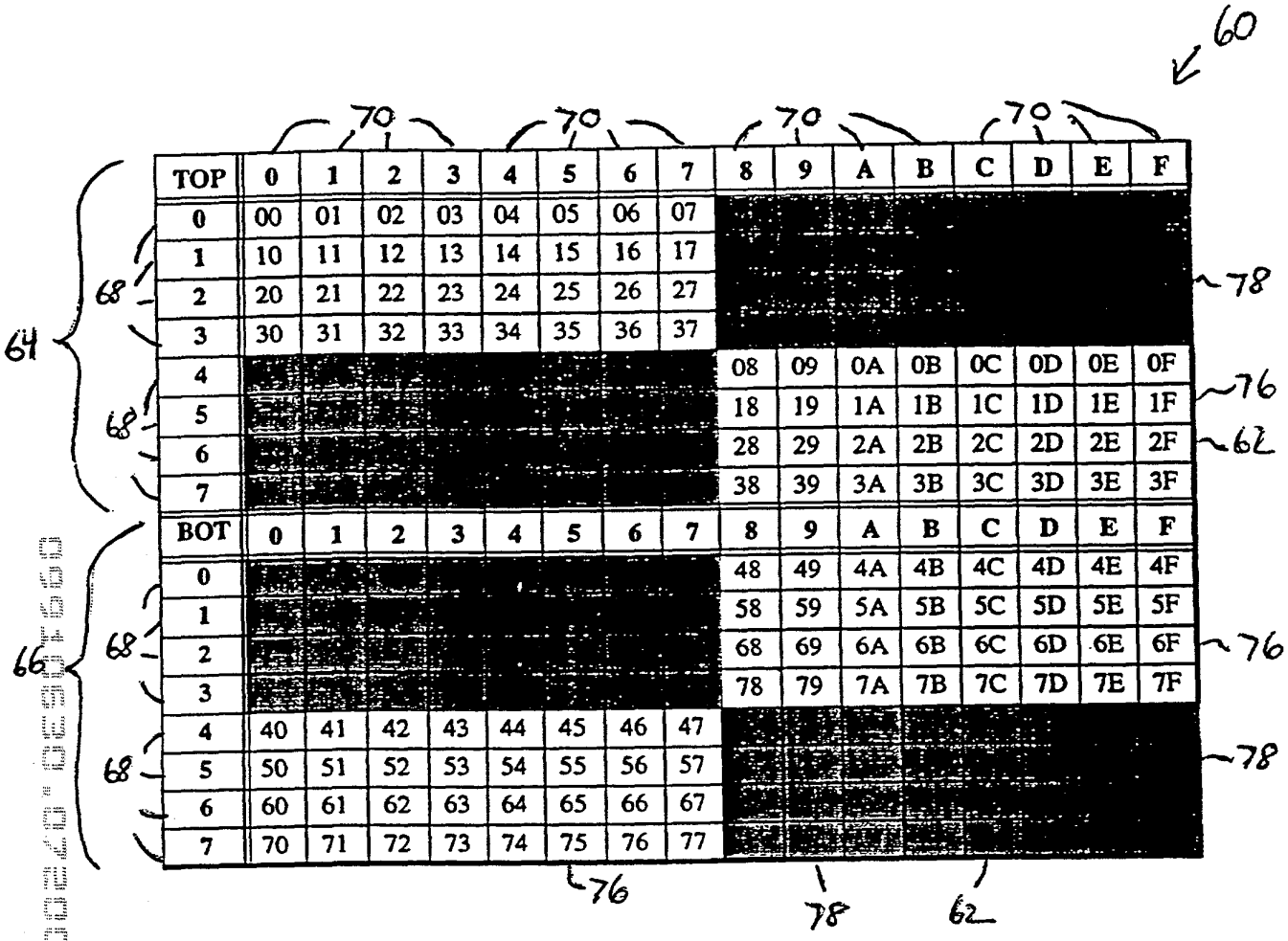


FIG. 8



F16. 9

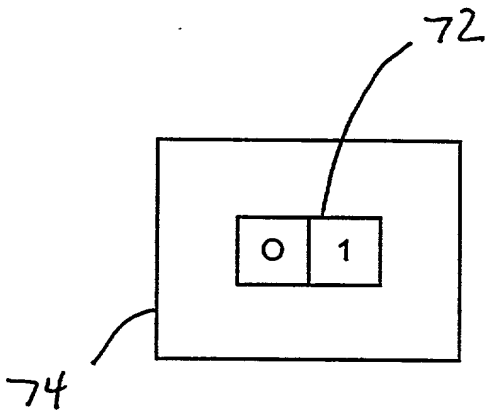


FIG. 10

NODE	HEX NOTATION	CORRESPONDING N BIT SETTINGS		
			Second Least Significant Bit	Least Significant Bit
		7	6 5 4	3 2 1 0
0	0_x00	0	0 0 0	0 0 0 0
19	0_x19	0	0 0 1	1 0 0 1

FIG. 11

NODE	SHARING MODE 11					
	PLANE N7	ROW N6,N5,N4	COL. N3 : N0	PLANE	ROW	COL.
0	0	000	0000	0	0	0
19	0	001	1001	0	1	9

FIG. 12

NODE	SHARING MODE 10					
	PLANE N6	ROW N6⊗N3,N5,N4	COL. N3 : N0	PLANE	ROW	COL.
0	0	000	0000	0	0	0
19	0	101	1001	0	5	9

FIG. 13